



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of Laurence B. Boucher, et al.      Serial No:      10/724,588  
Filing Date:      November 28, 2003      Examiner:      Wen Tai Lin  
Atty. Docket No:      ALA-025      GAU:      2154  
For:      INTELLIGENT NETWORK INTERFACE SYSTEM AND METHOD  
FOR ACCELERATED PROTOCOL PROCESSING

November 14, 2005

MS Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

4<sup>th</sup> Supplemental Information Disclosure Statement

Sir:

Pursuant to 37 C.F.R. §§ 1.56, 1.97 and 1.98, applicants bring thirteen documents listed on an enclosed one-page form PTO-1449 to the attention of the Examiner in the above-identified application. Enclosed are copies of ten non-patent reference documents and two foreign patent reference documents. A copy of the one U.S. patent reference document is not enclosed. A check in the amount of \$180 is enclosed, as required under 37 C.F.R. § 1.97(c)(2).

Citation of these documents shall not be construed as an admission that the documents are prior art with respect to the instant invention, a representation that a search has been made, or an admission that the information cited herein is, or is considered to be, material to patentability as defined in 37 C.F.R. § 1.56(b).

Respectfully submitted,

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450 Alexandria, VA 22313-1450, on November 14, 2005.

Date: 11-14-05

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U.S. Department of Commerce, Patent and Trademark Office	Application No.: 10/724,588
<b>04<sup>th</sup> SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>	Filing date: November 28, 2003
NOV 17 2005	Inventors: Boucher, et al.
INTELLIGENT NETWORK INTERFACE SYSTEM AND METHOD FOR ACCELERATED PROTOCOL PROCESSING	Group Art Unit: 2154
	Examiner name: Wen Tai Lin
	Attorney Docket No.: ALA-025

## U.S. Patent Documents

*Examiner Initial	Cite No.	Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
	1	6,067,569	5/23/00	Khaki et al.	709	224	

## Foreign Patent Documents

*Examiner Initial	Cite No.	Document Number	Date	Country	Class	Subclass	Yes	No
	2	WO 01/59966	8/16/01	PCT/US00/06475				
	3	WO 01/86430	11/15/01	PCT/US01/15180				

## Other Art—Non Patent Literature Documents

*Examiner Initial	Cite No.	(Including Author, Title, Date, Pertinent Pages, Etc.)
	4	Andrew S. Tanenbaum, Computer Networks, Third Edition, 1996, ISBN 0-13-349945-6.
	5	Rice University article entitled "LRP: A Network Subsystem Architecture for Server Systems", by P. Druschel and G. Banga, 14 pages.
	6	Internet RFC/STD/FYI/BCP Archives article with heading "RFC2140" entitled "TCP Control Block Interdependence", web address <a href="http://www.faqs.org/ftp/rfc/pdf/rfc2140.txt.pdf">http://www.faqs.org/ftp/rfc/pdf/rfc2140.txt.pdf</a> , 11 pages, April 1997.
	7	WindRiver article entitled "Tornado: For Intelligent Network Acceleration", copyright Wind River Systems, 2001, 2 pages.
	8	WindRiver White Paper entitled "Complete TCP/IP Offload for High-Speed Ethernet Networks", Copyright Wind River Systems, 2002, 7 pages.
	9	Intel article entitled "Solving Server Bottlenecks with Intel Server Adapters", Copyright Intel Corporation, 1999, 8 pages.
	10	Koufopavlou et al., IEEE Global Telecommunications Conference, Globecom '92, presentation entitled, "Parallel TCP for High Performance Communication Subsystems", 7 pages, Dec. 6-9, 1992.
	11	Thia, Y.H. Publication entitled "High-Speed OSI Protocol Bypass Algorithm with Window Flow Control", <u>Protocols for High Speed Networks</u> , pages 53-68, 1993.
	12	Thia, Y.H. Publication entitled "A Reduced Operational Protocol Engine (ROPE) for a multiple-layer bypass architecture", <u>Protocols for High Speed Networks</u> , pages 224-239, 1995.
	13	U.S. Provisional Patent Application No.: 60/053,240, Titled: TCP/IP Network Accelerator and Method of Use, Filed July 18, 1997, Inventors: Jolitz et al., Assignee: InterProphet LLC.

Examiner

Date Considered

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.